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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/803,965	03/19/2004	Jun Koyama	07977-209004	6486
26171	7590	08/11/2004	EXAMINER	
FISH & RICHARDSON P.C. 1425 K STREET, N.W. 11TH FLOOR WASHINGTON, DC 20005-3500			ROSE, KIESHA L	
			ART UNIT	PAPER NUMBER
			2822	

DATE MAILED: 08/11/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/803,965

Applicant(s)

KOYAMA ET AL.

Examiner

Kiesha L. Rose

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 3/19/04.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 10-39 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 10-39 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 3/19/2004.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

This Office Action is in response to the filing of the application.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 10-39 are rejected under 35 U.S.C. 102(e) as being anticipated by Takemura et al. (U.S. Patent 5,739,549).

Takemura discloses a thin film transistor (Figs. 2a-2f) that contains an aluminum first wiring line (205) (Column 9, lines 1-3) formed over a substrate, an aluminum second wiring line (220) (Column 9, line 53) formed over the first wiring line with an insulating film (218) (formed of polyimide, silicon nitride or interlayer insulating film)(Column 4, lines 12-23) interposed therebetween, the first wiring line and second wiring line extend in parallel with each other, the first wiring line and second wiring line are electrically connected with each other via a plurality of contact holes opened in the insulating film, a gate electrode (207) formed on the same layer as the first wiring line of the thin film transistor, a source electrode (223) formed on the same layer as the

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second wiring line, wherein the width of the first wiring line (3000-8000Å) is smaller than the second wiring line (5000Å). (Column 8, lines 56-58, Column 9, lines 53-55)

Claims 10,16 and 22 are rejected under 35 U.S.C. 102(e) as being anticipated by Matsushima et al. (U.S. Patent 5,744,820).

Matsushima discloses a display device (Fig. 14b) that contains a first wiring line (135) formed over a substrate (21), a second wiring line formed over the first wiring line with an insulating film interposed therebetween, the first wiring line and second wiring line extend in parallel with each other, the first wiring line and second wiring line are electrically connected with each other via a plurality of contact holes opened in the insulating film, a gate electrode (131) formed on the same layer as the first wiring line of the thin film transistor, a source electrode (132) formed on the same layer as the second wiring line.

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 10,12-13,16,18-19,22 and 24-25 are rejected under 35 U.S.C. 102(b) as being anticipated by Shimada et al. (U.S. Patent 5,182,620).

Shimada discloses a display device (Fig. 2) that contains a first wiring line (6) formed over a substrate (11), a second wiring line (8) formed over the first wiring line with an insulating film (14) formed of silicon nitride or an interlayer insulating film (Column 5, lines 59-60) interposed therebetween, the first wiring line and second wiring line extend in parallel with each other, the first wiring line and second wiring line are electrically connected with each other via a plurality of contact holes (7c) opened in the

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insulating film, a gate electrode (3a) formed on the same layer as the first wiring line of the thin film transistor, a source electrode (2) formed on the same layer as the second wiring line.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kiesha L. Rose whose telephone number is 571-272-1844. The examiner can normally be reached on M-F 8:30-6:00 off 2nd Mondays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


KLR


MARIA F. GUERRERO
PRIMARY EXAMINER